



14th International Workshop on **CHARACTERIZATION AND MODELING OF MEMORY DEVICES**

October 2-3, 2025 | MILANO, ITALY

University of Milano-Bicocca, "Aula Martini" Building U6
Piazza dell'Ateneo Nuovo 1

PROGRAMME

October, 2

9.30 Registration

10.15 **Paolo Cappelletti Award**

10.30 **David Refaldi** (Politecnico di Milano)

Exploring 3D NAND Flash memory operation in the cryogenic regime

11.15 **Rino Micheloni** (Avaneidi Spa, Italy) - **Cristian Zambelli** (University of Ferrara)

Linking 3D NAND Flash with Solid State Drives reliability: a holistic device-to-system approach

12.00 **Yiyi Wang** (NVIDIA, Los Altos, California)

Beyond Traditional Simulation: A GPU-Powered and AI-Driven Framework for Memory Device Modeling

12.45 Lunch

14.00 **Alessandro Grossi** (Infineon, Padova)

RRAM for automotive applications: reliability challenges and outlook

14.45 **Sergiu Clima** (IMEC, Belgium)

Understand Atomic Mechanisms and Predict Superior OTS Materials for Selector-Only-Memory with First-Principles Simulations

15.30 **Valeria Bragaglia** (IBM, Zurich)

A Novel ReRAM for Analog Training/Inference Accelerator and BEOL Non-Volatile Memories

16.15 Coffee break

October, 3

9.15 **Thomas Boesch** (STMicroelectronics)

eNVM vs SRAM In-Memory-Computing for embedded neural network processing

10.00 **Alessandro Calderoni** (Micron Technology)

Past, Present, and Future of DRAM Technology: Challenges, Innovations and Emerging Technologies

10.45 Coffee break

11.15 **Asif Khan** (Georgia Tech., Atlanta)

Pushing the limits of NAND scaling with Ferroelectrics

12.00 **Pouya Esmaili Dokht** (BSC, Barcelona)

MESS benchmark

12.45 Lunch

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